



PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): En-Hsing Chen, et al.

Title: NAND MEMORY ARRAY INCORPORATING MULTIPLE SERIES
SELECTION DEVICES AND METHOD FOR OPERATION OF SAME

Application No.: 10/729,865

Filed:

December 5, 2003

Examiner: Nguyen, Van Thu T.

Group Art Unit:

2824

Atty. Docket No.: 023-0029

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8494

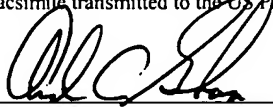
December 7, 2005

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**RESPONSE TO RESTRICTION REQUIREMENT**

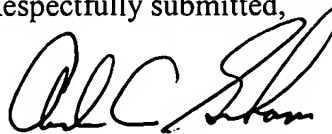
This paper is responsive to the Office action dated November 7, 2005, having a shortened statutory period for reply ending on December 7, 2005.

Election of Invention/Species

In response to the Examiner's restriction/election requirement, Applicant elects Group I, Species 3, being pending claims 1, 21, and 24-28, as grouped by the Examiner.

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 Andrew C. Graham	<u>12-7-05</u> Date

Respectfully submitted,

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